

## REMARKS/ARGUMENTS

Claims 2-6, 8, 10-12, and 14-16 are pending in the application; reexamination and reconsideration are hereby requested.

Claims 2-6, 8, 10-12, and 14-16 were objected to for informalities and rejected under 35 USC § 112.

Applicant replies that the claims have been amended as suggested by the Examiner. Further, the “logic level write drive component signal” of claim 10 and the “boost current write drive component signal” of claim 11 have been replaced by “direct current signal” and “boost signal”, respectively, to conform with application page 5, line 26 and page 7, line 4 terminology for  $I_{WDC}$  and  $I_{BSTREF}$ , respectively. And similarly for claims 12 and 14-16. The Examiner is correct that the “control signals” relate to the WDHX1 and BSTHX1 type signals.

The Appendix contains replacement sheets of drawings. Note that the reference numbers of Fig.1 items also appearing in Fig.2 are incremented by 40; thus the write head changed from 12 to 52, the impedance matching changed from 42 to 82, etc.

Respectfully submitted,

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